



Published on *CERN openlab* (<http://test-static-05.web.cern.ch>)

[Home](#) > Efficient computing with MMX and Integer SSE instructions

---

## Efficient computing with MMX and Integer SSE instructions <sup>[1]</sup>

### Author(s):

J. Dana


### Date presented:

Tuesday, 24 June, 2008

### Event presented at:

openlab II technical review meetings

### Presentation file:

 [Efficient computing with MMX and Integer SSE instructions.pdf](#) <sup>[2]</sup>

- [Visit Us](#)
- [RSS Feeds](#)

DISCLAIMER: This Web page contains pointers to material related to the management of CERN openlab in the Information Technology Department at the European Organization for Nuclear Research (CERN). Their use and distribution are regulated by the [CERN copyright notice](#).



---

**Source URL:** <http://test-static-05.web.cern.ch/publications/presentations/efficient-computing-mmx-and-integer-sse-instructions>

### Links

[1] <http://test-static-05.web.cern.ch/publications/presentations/efficient-computing-mmx-and-integer-sse->

instructions

[2] [http://test-static-05.web.cern.ch/sites/test-static-](http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/presentations/Efficient%20computing%20with%20MMX%20and%20Integer%20SSE%20instructions)

[05.web.cern.ch/files/presentations/Efficient%20computing%20with%20MMX%20and%20Integer%20SSE%20instructions](http://test-static-05.web.cern.ch/files/presentations/Efficient%20computing%20with%20MMX%20and%20Integer%20SSE%20instructions)