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prnewswire.com

SAN JOSE, Calif., June 22, 2016 /PRNewswire/ -- Integrated Device Technology, Inc. (IDT®) (NASDAQ: IDTI) announced today that CERN's data-processing framework has successfully been ported to a RapidIO®-enabled low-latency network that features multiple x86 CPU nodes, a world first in the high-performance computing market. The development comes during the first phase of IDT's collaboration with CERN (the European Organization for Nuclear Research), part of a multi-year project announced last year that is designed to speed and improve data analytics at CERN's Large Hadron Collider (LHC) and data center.

Link:

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[3] [http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/press-coverage/Y/M/IDT%20and%20CERN%20openlab%20Mark%20Milestone%20for%20Data%20Acquisition%20and%](http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/press-coverage/Y/M/IDT%20and%20CERN%20openlab%20Mark%20Milestone%20for%20Data%20Acquisition%20and%20)