



Published on *CERN openlab* (<http://test-static-05.web.cern.ch>)

[Home](#) > Evaluating the transport layer of the ALFA framework for the Intel® Xeon Phi? Coprocessor

Evaluating the transport layer of the ALFA framework for the Intel® Xeon Phi? Coprocessor ^[1]

Date published:

Friday, 13 November, 2015

Document type:

Journal paper

Author(s):

A. Santogidis

Co-Author(s):

A. Hirstius

S. Lalis


The ALFA framework supports the software development of major High Energy Physics experiments. As part of our research effort to optimize the transport layer of ALFA, we focus on profiling its data transfer performance for inter-node communication on the Intel Xeon Phi Coprocessor. In this article we present the collected performance measurements with the related analysis of the results. The optimization opportunities that are discovered, help us to formulate the future plans of enabling high performance data transfer for ALFA on the Intel Xeon Phi architecture.

Event published at:

21st International Conference on Computing in High Energy and Nuclear Physics (CHEP2015)

[Journal of Physics: Conference Series, volume 664](#) ^[2]

Technical document file:

 [pdf-2.pdf](#) ^[3]

- [Visit Us](#)
- [RSS Feeds](#)

DISCLAIMER: This Web page contains pointers to material related to the management of CERN openlab in the Information Technology Department at the European Organization for Nuclear Research (CERN). Their use and distribution are regulated by the CERN copyright notice.



Source URL: http://test-static-05.web.cern.ch/publications/technical_documents/evaluating-transport-layer-alfa-framework-intel%C2%AE-xeon-phi%E2%84%A2

Links

[1] http://test-static-05.web.cern.ch/publications/technical_documents/evaluating-transport-layer-alfa-framework-intel%C2%AE-xeon-phi%E2%84%A2

[2] <http://iopscience.iop.org/article/10.1088/1742-6596/664/9/092021>

[3] http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/technical_documents/pdf-2.pdf