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## LHC to Use RapidIO Interface for Big Data Transfers <sup>[1]</sup>

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**Outlet:**


electronicsweekly.com

CERN, Europe's nuclear research facility, has worked with US-based interface IC firm Integrated Device Technology to create a data bus platform with the necessarily low latency needed for data analytics at the organisation's Large Hadron Collider (LHC) and data centre.

**Link:**

[Article on electronicsweekly.com](#) <sup>[2]</sup>

**Copy of the coverage:**

 [LHC to use RapidIO interface for big data transfers | Electronics Weekly.pdf](#) <sup>[3]</sup>

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data-transfers

### **Links**

[1] [http://test-static-05.web.cern.ch/resources/press\\_coverage/lhc-use-rapidio-interface-big-data-transfers](http://test-static-05.web.cern.ch/resources/press_coverage/lhc-use-rapidio-interface-big-data-transfers)

[2] <http://www.electronicweekly.com/news/lhc-to-use-rapidio-interface-for-big-data-transfers-2015-12/>

[3] <http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/press-coverage/Y/M/LHC%20to%20use%20RapidIO%20interface%20for%20big%20data%20transfers%20%7C%20Electr>