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This report discusses the details of the architecture of a performance monitoring tool on an x86 processor. Although modern CPUs have Performance Monitoring Units (PMUs) that are relatively easy to program, collecting and interpreting the data requires a fair amount of complexity. The discussion involves the issues of obtaining and storing the raw performance data and the aspects of their subsequent analysis.

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Technical document file:

 [Urs_Fassler_report.pdf](#) ^[3]

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