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## Optimizing the Transport Layer of ALFA on the Intel Xeon Phi Coprocessor <sup>[1]</sup>

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### Event presented at:

Okinawa Institute of Science and Technology (OIST), Okinawa, Japan

[For more information](#) <sup>[2]</sup>

### Presentation file:

 [Track8\\_Aram\\_Santogidis.pptx](#) <sup>[3]</sup>

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