



Published on *CERN openlab* (<http://test-static-05.web.cern.ch>)

[Home](#) > WP2 - Reconfigurable Computing and FPGAs

WP2 - Reconfigurable Computing and FPGAs ^[1]

Author(s):

S. Sridharan

Date presented:

Friday, 25 July, 2014

Event presented at:

Online, Intel Munich, Germany

Presentation file:

 [Intel%20Presentation%202.pptx](#) ^[2]

- [Visit Us](#)
- [RSS Feeds](#)

DISCLAIMER: This Web page contains pointers to material related to the management of CERN openlab in the Information Technology Department at the European Organization for Nuclear Research (CERN). Their use and distribution are regulated by the [CERN copyright notice](#).



Source URL: <http://test-static-05.web.cern.ch/publications/presentations/wp2-reconfigurable-computing-and-fpgas>

Links

[1] <http://test-static-05.web.cern.ch/publications/presentations/wp2-reconfigurable-computing-and-fpgas>

[2] <http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/presentations/Intel%20Presentation%202.pptx>