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## Dynamically Adaptive Header Generator and Front-End Source Emulator for a 100 Gbps FPGA based DAQ <sup>[1]</sup>

### Author(s):

S. Sridharan

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[Real-Time Conference \(2014\)](#) <sup>[2]</sup>

### Presentation file:

 [PS3-27-Srikanth.pdf](#) <sup>[3]</sup>

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