



Published on *CERN openlab* (<http://test-static-05.web.cern.ch>)

[Home](#) > Dynamically Adaptive Header Generator and Front- End Source Emulator for a 100 Gbps FPGA based DAQ

Dynamically Adaptive Header Generator and Front- End Source Emulator for a 100 Gbps FPGA based DAQ ^[1]

Date published:

Wednesday, 28 May, 2014

Document type:

Poster

Author(s):

S. Sridharan


The Header Generator module is used to packetize the streaming data from the detector's Front-End electronics before it is sent to the PCs for further processing. The objective is to create a meta header or an index by creating packets out of 100s of small event dataset.

Event published at:

Real-Time Conference (2014)

[Real-Time Conference \(2014\)](#) ^[2]

Technical document file:

 [Poster-PS3-27-Srikanth.pdf](#) ^[3]

- [Visit Us](#)
- [RSS Feeds](#)

DISCLAIMER: This Web page contains pointers to material related to the management of CERN openlab in the Information Technology Department at the European Organization for Nuclear Research (CERN). Their use and distribution are regulated by the [CERN copyright notice](#).



Source URL: http://test-static-05.web.cern.ch/publications/technical_documents/dynamically-adaptive-header-generator-and-front-end-source-emulator

Links

[1] http://test-static-05.web.cern.ch/publications/technical_documents/dynamically-adaptive-header-generator-and-front-end-source-emulator

[2] <http://rt2014.rcnp.osaka-u.ac.jp/program.html>

[3] http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/technical_documents/Poster-PS3-27-Srikanth.pdf