Experiments with multi-threaded velopixel track reconstruction

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Omar Awile (omar.awile@cern.ch),

Pawel Szostek



Background image: Shutterstock



A Thread-Parallel Implementation of High-Energy Physics Particle Tracking on Many-Core Hardware Platforms



Online Computing Challenges at the LHC

Basic constraints:

- limited money
- limited power
- limited manpower

Network – Projected Throughput [Tbit/s]



Challenges:

- Hard time constraints @ high throughput 40MHz readout rate at trigger (for LHCb).
- The task of the Trigger and Data Acquisition (DAQ) is to reduce data volume for LHC experiments 100 TB/s \rightarrow 100 PB/yr (factor ~25k).
- While filtering for and reconstructing interesting events.



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Challenges for trigger and DAQ upgrade

L1 Trigger

- High efficiency despite overlapping collisions add tracking information
- Flexible, robust and easy to reproduce
- Algorithms must process ~10'000 events/s





For the example of LHCb

DAQ

- Collision data spread over 10'000 pieces
- Data gathered onto one of 1000s compute units
- Compute units run complex filter algorithms

High-Level Trigger

- large software infrastructure
- flat time profile
- complex and costly algorithms for reconstruction
- difficult to parallelize algorithms







Thread-parallel track reconstruction

- Triggering is parallelized by running multiple (serial) instances of code
- We want to explore how track reconstruction for vertex locator data can be done on multi- and manycore CPUs using multithreading.





Thread-parallel track reconstruction

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 Host-mode manycore processors (Knights Landing) with 100s of HW threads are around the corner, how can we scale that far?





OpenMP



TBB



VeloPixel track reconstruction

- Iterative algorithm that finds straight lines in collision event data in VeloPixel subdetector.
 - Triplets of hits with best criterion are searched (seeding)
 - Triplets are extended to tracks if a fitting hit can be found







Using OpenMP and TBB for multilevel parallelism

- We would like to be able to compare our parallel code with a typical production run.
 - --> we parallelize over events and within each event
- · OpenMP uses nested parallelism, parallel for
- TBB For now mostly based on parallel_for
 - Also tested pipelining
- Used lock-free parallel implementations
 - TBB thread-safe data-structures did not perform well!





Results and Timings





Recovering track reconstruction efficiency

Production code aka Brunel (v50r0) PrPixel			
2248492 tracks including 56641 ghosts	2.5%). Event average 1.9	8	
velo : 1937720 from 210549	3 (92.0%) 44013 clones (2.27%), purity: (99.81%),	hitEff: (95.40%)
long : 672751 from 67862	8 (99.1%) 13556 clones (2.02%), purity: (99.82%),	hitEff: (96.72%)
long>5GeV : 446458 from 44853	5 (99.5%) 7731 clones (1.73%), purity: (99.83%),	hitEff: (97.25%)
long_strange : 27383 from 2784	6 (98.3%) 416 clones (1.52%), purity: (99.33%),	hitEff: (97.51%)
long_strange>5GeV : 13436 from 1367	9 (98.2%) 128 clones (0.95%), purity: (99.16%),	hitEff: (98.35%)
long_fromb : 38897 from 3914	8 (99.4%) 690 clones (1.77%), purity: (99.78%),	hitEff: (97.15%)
long_fromb>5GeV : 32074 from 3219	6 (99.6%) 537 clones (1.67%), purity: (99.80%),	hitEff: (97.36%)

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2180404 tracks incl	ludi	ing 26	5268 g	hosts (1.	.2%). Ever	nt avera	age 1.	0%							
velo	1.1	1923734	from	2105493	(91.4%)	30356	clones	(1.58%),	purity:	(99).77%),	hitEff:	(96.06%)
long	÷ 1	671727	from	678628	(99.0%)	8266	clones	(1.23%),	purity:	(99).74%),	hitEff:	('	97.75%)
long>5GeV	1.1	445784	from	448535	(99.4%)	4672	clones	(1.05%),	purity:	(99	.78%),	hitEff:	(98.26%)
long_strange	:	27152	from	27846	(97.5%)	320	clones	(1.18%),	purity:	(99).21%),	hitEff:	('	97.81%)
long_strange>5GeV	÷	13365	from	13679	(97.7%)	116	clones	(0.87%),	purity:	(99	.06%),	hitEff:	('	98.55%)
long_fromb	0.	38778	from	39148	(99.1%)	368	clones	(0.95%),	purity:	(99).70%),	hitEff:	('	97.94%)
long_fromb>5GeV	:	31989	from	32196	(99.4%)	275	clones	(0.86%),	purity:	(99).73%),	hitEff:	('	98.15%)





• tbbPixel speedup HT: 1.29

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Timings



OpenMP Timings

- Runtime very sensitive to scheduling policies (dynamic vs static, granularities)
- Nested parallel regions often give a slow-down with respect to non-nested parallelism





- Scalability of tbbPixel (or ompPixel) is limited!
 - Event execution times vary by up to x50
 —> computational imbalance
- For now we mostly parallelized simple loops

--> we are limited by Amdahl's law

Scalability issues



 A majority of events are very small, loop trip-counts are very small

--> overhead from multithreading can be significant





- Xeon-Phi Knights Landing:
 - We have started testing/benchmarking!
 - With 200+ threads scaling is a problem
- TBB Flow Graph or HPX?
 - Express our algorithm in terms of small concurrent tasks
 - Leave the rest up to scheduler
- How can we reduce computational imbalance?
 - Process "small" events only in serial freeing up resources for "big" events
- Understand scaling problems in OpenMP



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Thank you!

Who are we:

CERN openlab High Throughput Computing Collaboration

Olof Bärring, Niko Neufeld Omar Awile, Paolo Durante, Christian Färber, Karel Hà, Jon Machen (Intel), Rainer Schwemmer, Srikanth Sridharan, Paweł Szostek, Sébastien Valat, Balázs Vőneki













Backup



16 ackground image: Shutterstock





LHCb upgrade (2020)

- LHCb studies CP-violation, rare decays, ...
- The upgrade:
 - Currently readout of detector @ 1MHz After upgrade: 40MHz
 - Redesign of DAQ system
 - yield > 10x more events





