



Published on *CERN openlab* (<http://test-static-05.web.cern.ch>)

[Home](#) > [Blog: Intel® Xeon® Processor 5000 Sequence](#)

Blog: Intel® Xeon® Processor 5000 Sequence ^[1]


[Intel](#) ^[2]

Link:

[Blog: Intel® Xeon® Processor 5000 Sequence](#) ^[3]

Tuesday, 4 November, 2008

The breakthrough performance, energy efficiency, and reliability of Intel® Xeon® processor-based server systems make them the ideal choice for all of your data demanding or standard enterprise infrastructure applications.

 [intel_Intel® Xeon® Process_BLOG.pdf](#) ^[4]

Phase:

[openlab phase II](#) ^[5]

Competence center:

[Platform](#) ^[6]

- [Visit Us](#)
- [RSS Feeds](#)

DISCLAIMER: This Web page contains pointers to material related to the management of CERN openlab in the Information Technology Department at the European Organization for Nuclear Research (CERN). Their use and distribution are regulated by the [CERN copyright notice](#).



Links

- [1] <http://test-static-05.web.cern.ch/resources/spotlights/blog-intel%C2%AE-xeon%C2%AE-processor-5000-sequence>
- [2] http://test-static-05.web.cern.ch/about/industry_members/intel
- [3] <http://apnaintel.blogspot.com/2008/11/intel-xeon-processor-5000-sequence.html>
- [4] http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/spotlights/2012/intel_%20Intel%C2%AE%20Xeon%C2%AE%20Process_BLOG.pdf
- [5] <http://test-static-05.web.cern.ch/about/phase-ii>
- [6] <http://test-static-05.web.cern.ch/competence-centre/platform>