



Published on *CERN openlab* (<http://test-static-05.web.cern.ch>)

[Home](#) > Performance Analysis and SW optimization for HPC on Intel® Core? i7, Xeon? 5500 and 5600 family Processors

---

## Performance Analysis and SW optimization for HPC on Intel® Core? i7, Xeon? 5500 and 5600 family Processors <sup>[1]</sup>

### Author(s):

D. Levinthal

### Date presented:

Wednesday, 21 July, 2010

### Event presented at:

Advanced session on performance monitoring with Intel

[Advanced session on performance monitoring with Intel](#) <sup>[2]</sup>

### Presentation file:

 [HPC\\_Perf\\_analysis\\_Xeon\\_5500\\_5600\\_intro.pdf](#) <sup>[3]</sup>

- [Visit Us](#)
- [RSS Feeds](#)

DISCLAIMER: This Web page contains pointers to material related to the management of CERN openlab in the Information Technology Department at the European Organization for Nuclear Research (CERN). Their use and distribution are regulated by the [CERN copyright notice](#).



---

**Source URL:** <http://test-static-05.web.cern.ch/publications/presentations/performance-analysis-and-sw-optimization-hpc-intel%C2%AE-core%E2%84%A2-i7-xeon%E2%84%A2-5500>

**Links**

- [1] <http://test-static-05.web.cern.ch/publications/presentations/performance-analysis-and-sw-optimization-hpc-intel%C2%AE-core%E2%84%A2-i7-xeon%E2%84%A2-5500>
- [2] [https://openlab-mu-internal.web.cern.ch/openlab-mu-internal/00\\_News/News\\_pages/2010/10-15\\_Meeting\\_David\\_Levinthal/10-15\\_Meeting\\_David\\_Levinthal.htm](https://openlab-mu-internal.web.cern.ch/openlab-mu-internal/00_News/News_pages/2010/10-15_Meeting_David_Levinthal/10-15_Meeting_David_Levinthal.htm)
- [3] [http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/presentations/HPC\\_Perf\\_analysis\\_Xeon\\_5500\\_5600\\_intro.pdf](http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/presentations/HPC_Perf_analysis_Xeon_5500_5600_intro.pdf)