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IDT and CERN Engineer Low-Latency Platform to Speed and Improve Analytics at Large Hadron Collider ^[1]

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hpcwire.com

SAN JOSE, Calif., Dec. 15 ? Integrated Device Technology, Inc. ([IDT](#) ^[2]) (NASDAQ: [IDTI](#) ^[3]) announced today that it has developed with the European Organization for Nuclear Research (CERN) a low-latency platform to speed and improve the management of analytics at the organization's Large Hadron Collider (LHC) and data center. Developed at IDT's Open HPAC Lab and built upon the company's RapidIO technology, the platform marks the first major milestone in the three-year collaboration IDT and CERN openlab announced in [March](#) ^[4].

Link:

[Article on hpcwire.com](#) ^[5]

Copy of the coverage:

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Links

[1] http://test-static-05.web.cern.ch/resources/press_coverage/idt-and-cern-engineer-low-latency-platform-speed-and-improve-analytics

[2]

http://www.idt.com/?utm_campaign=interface_connectivity&utm_source=press_release&utm_medium=pre

[3] <http://studio-5.financialcontent.com/prnews?Page=Quote&Ticker=IDTI>

[4] [http://www.idt.com/about/press-room/idt-collaborates-cern-speed-and-improve-data-analytics-large-hadron-collider-and-data-](http://www.idt.com/about/press-room/idt-collaborates-cern-speed-and-improve-data-analytics-large-hadron-collider-and-data-center?utm_campaign=interface_connectivity&utm_source=press_release&utm_medium=press_release&)

[center?utm_campaign=interface_connectivity&utm_source=press_release&utm_medium=press_release&](http://www.idt.com/about/press-room/idt-collaborates-cern-speed-and-improve-data-analytics-large-hadron-collider-and-data-center?utm_campaign=interface_connectivity&utm_source=press_release&utm_medium=press_release&)

[5] http://www.hpcwire.com/off-the-wire/idt-cern-engineer-low-latency-platform-speed-improve-analytics-large-hadron-collider/?utm_source=rss&utm_medium=rss&utm_campaign=idt-cern-engineer-low-latency-platform-speed-improve-analytics-large-hadron-collider

[6] [http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/press-coverage/Y/M/IDT%20and%20CERN%20Engineer%20Low-](http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/press-coverage/Y/M/IDT%20and%20CERN%20Engineer%20Low-Latency%20Platform%20to%20Speed%20and%20Improve%20Analytics%20at%20Large%20Hadron%20Collider.pdf)

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