



Published on *CERN openlab* (<http://test-static-05.web.cern.ch>)

[Home](#) > Evaluation of Power Efficiency and Performance of Multi-core Platforms Using HEP Workloads

Evaluation of Power Efficiency and Performance of Multi-core Platforms Using HEP Workloads ^[1]

Date published:

Wednesday, 10 June, 2015

Document type:

Poster

Author(s):

P. Szostek

Haswell-EP is the latest dualsocket computing platform from Intel. In our tests we compare it with the preceding families: Sandy Bridge-EP and Ivy Bridge-EP. We put particular focus on the power efficiency and Cluser-on-die - new memory management feature.

Event published at:

openlab Open Day

[For more information](#) ^[2]

Technical document file:

 [pawel.szostek.openday.poster.pdf](#) ^[3]

- [Visit Us](#)
- [RSS Feeds](#)

DISCLAIMER: This Web page contains pointers to material related to the management of CERN openlab in the Information Technology Department at the European Organization for Nuclear Research (CERN). Their use and distribution are regulated by the [CERN copyright notice](#).



Source URL: http://test-static-05.web.cern.ch/publications/technical_documents/evaluation-power-efficiency-and-performance-multi-core-platforms

Links

[1] http://test-static-05.web.cern.ch/publications/technical_documents/evaluation-power-efficiency-and-performance-multi-core-platforms

[2] <http://test-static-05.web.cern.ch/news/cern-openlab-open-day>

[3] http://test-static-05.web.cern.ch/sites/test-static-05.web.cern.ch/files/technical_documents/pawel_szostek_openday_poster.pdf