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## CERN openlab/Intel Parallelism, Compilers and Performance Workshop <sup>[1]</sup>

### **Date:**

Monday, 12 May, 2014 (All day) to Wednesday, 14 May, 2014 (All day)

### **Location:**

CERN Building 30 Kjell Johnsen Auditorium <sup>[2]</sup>

The aim of the lectures and exercises contained in this 3-day workshop is to give the attendees a practical introduction to parallel programming, performance optimization and compilers. Linux systems on modern servers will be used as a base. Several experts, including one from Intel, will lead the event and help you improve your knowledge by providing a complete view of the modern PC computing platform. Hands-on labs are also planned where participants will be able to improve their understanding of the course in practical environments, guided by the speakers.

Key intricacies of parallel programming will be explained, along with the most efficient solutions to common multi-threading problems, using modern parallelization environments compatible with C and C++. Performance tuning and computer architecture are also important topics on the agenda, closely linked to the seven performance dimensions of PC servers.

### **Pre-requisites:**

- Familiarity with the C and C++ programming languages
- Knowledge of Linux
- Basic understanding of modern computer architecture and compilers

Morning lectures will be held in the Kjell Johnsen Auditorium in bldg. 30, while afternoon labs in the Technical Training Center (bldg. 593), starting at 9 am on all three days.

### **Indico or other event webpage:**

To attend, please register in EDH (first come first served) <sup>[3]</sup>

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[3] <http://edh.cern.ch/Document/Personnel/TRN/new?course=123PCP01>