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Xeon Phi workshop ^[1]

Date:

Thursday, 11 April, 2013 (All day) to Friday, 12 April, 2013 (All day)

Location:

CERN ^[2]

We are planning a dedicated, hands-on Intel MIC / Xeon Phi course at CERN, organized by openlab and delivered by two experts from Intel on April 11th-12th. The session will be at an introductory/intermediate level, and it will be an excellent opportunity to ask the knowledgeable tutors questions. The tentative agenda, which will be adjusted based on your comments, is as follows:

- Hardware and software overview
- Programming models, including threading, vectorization, offloading, MPI
- Performance analysis with VTune and Intel Trace Analyzer/Collector
- Hands-on labs including offload memory and MPI exercises

Indico or other event webpage:

More Information about the event ^[3]

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Links

- [1] <http://test-static-05.web.cern.ch/news/xeon-phi-workshop>
- [2] <http://maps.cern.ch>
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