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Performance productivity challenges and researches for the future of computing ^[1]

Date:

Friday, 30 August, 2013 - 11:15 to 12:15

Location:

[31-3-004 - IT Auditorium](#) ^[2]

Exa-scale computing remains being an achievable target within this decade due to the steady progress brought by advances in computer architecture, chip design and manufacturing. Intel is an obvious key player in this area. But in order to seize to its full extent all this potential power, it also requires a radical shift in software design and development. Parallelism is one of the prime bets at Intel. As part of the fruitful openlab partnership with Intel, Andrzej Nowak is hosting Victor Lee, research scientist at Intel's Parallel Computing Lab, key contributor to the Intel Many Integrated Core architecture and a member of the Intel Xeon Phi coprocessor design team.

CERN has taken the occasion to arrange a presentation where Victor will discuss the challenges of performance productivity and how academia and industry are addressing them. It will take place in the form of a seminar scheduled for Friday the 30th of August.

Indico or other event webpage:

[More Information about the event](#) ^[3]

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